

Figure 1 (Prior Art)

FIG. 2 is a block diagram of a network architecture. The architecture includes a Network Server (105(a)), a Mainframe (105(b)), and a PC/Unix Box (105(c)) connected to a Switching Fabric (110). The Switching Fabric (110) is connected to a Switching Fabric (115). The Switching Fabric (115) is connected to a Disk Array 1 (120(a)) and a Disk Array 2 (120(b)). The Switching Fabric (110) is also connected to an Adjunct Processor (200). The Adjunct Processor (200) includes a component 210 and a component 215.

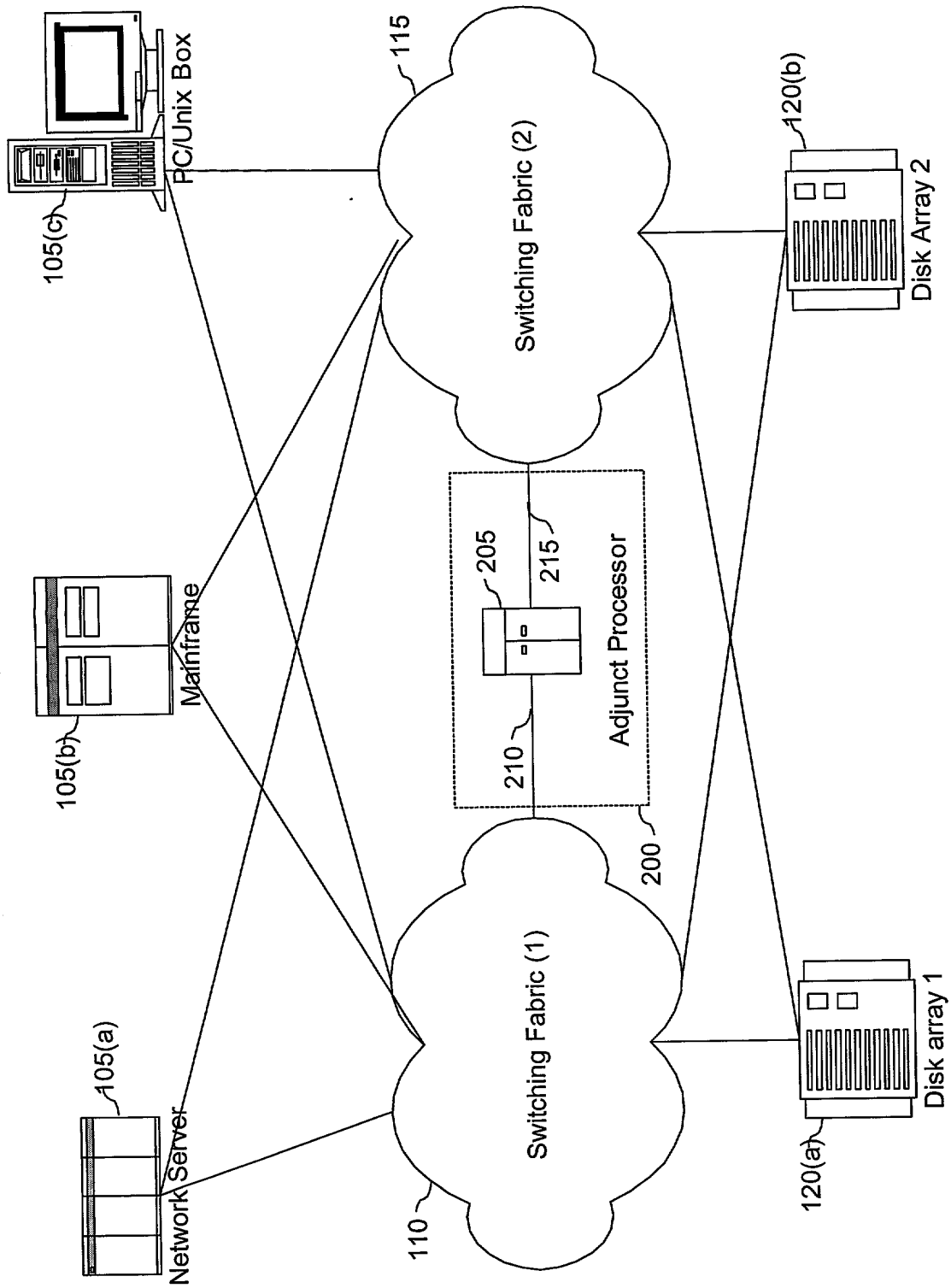


Figure 2

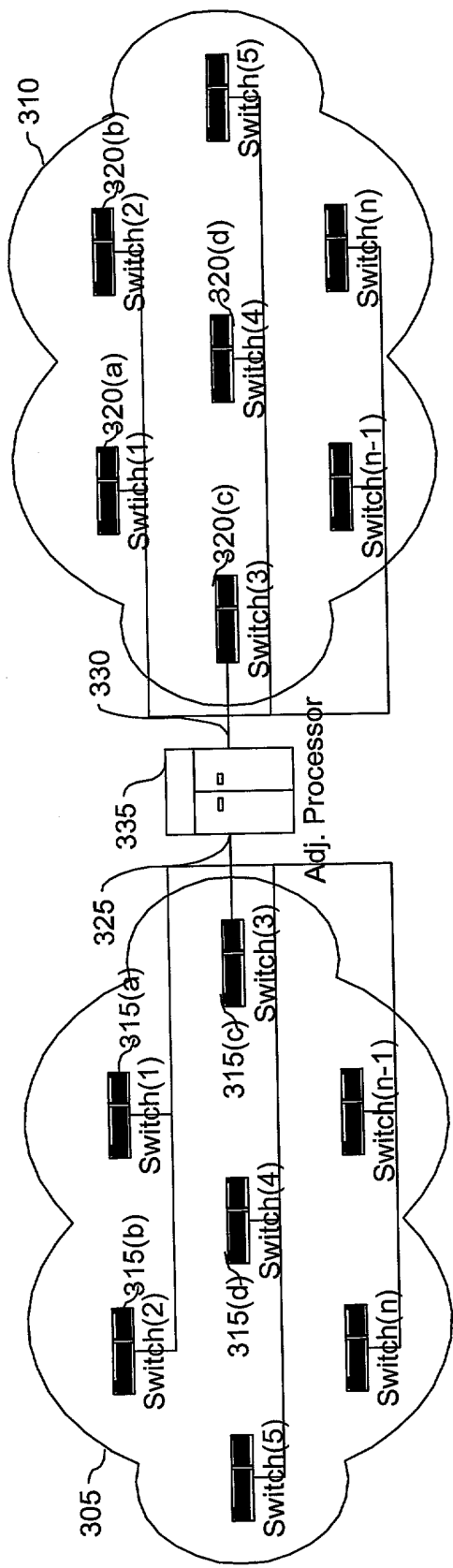


Figure 3A

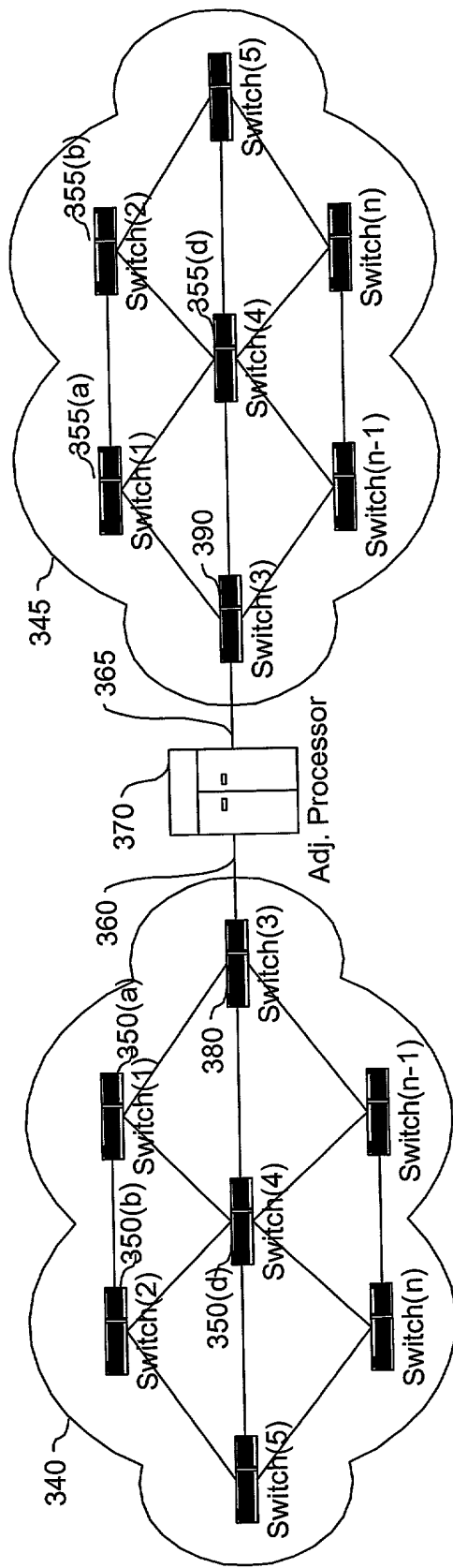


Figure 3B

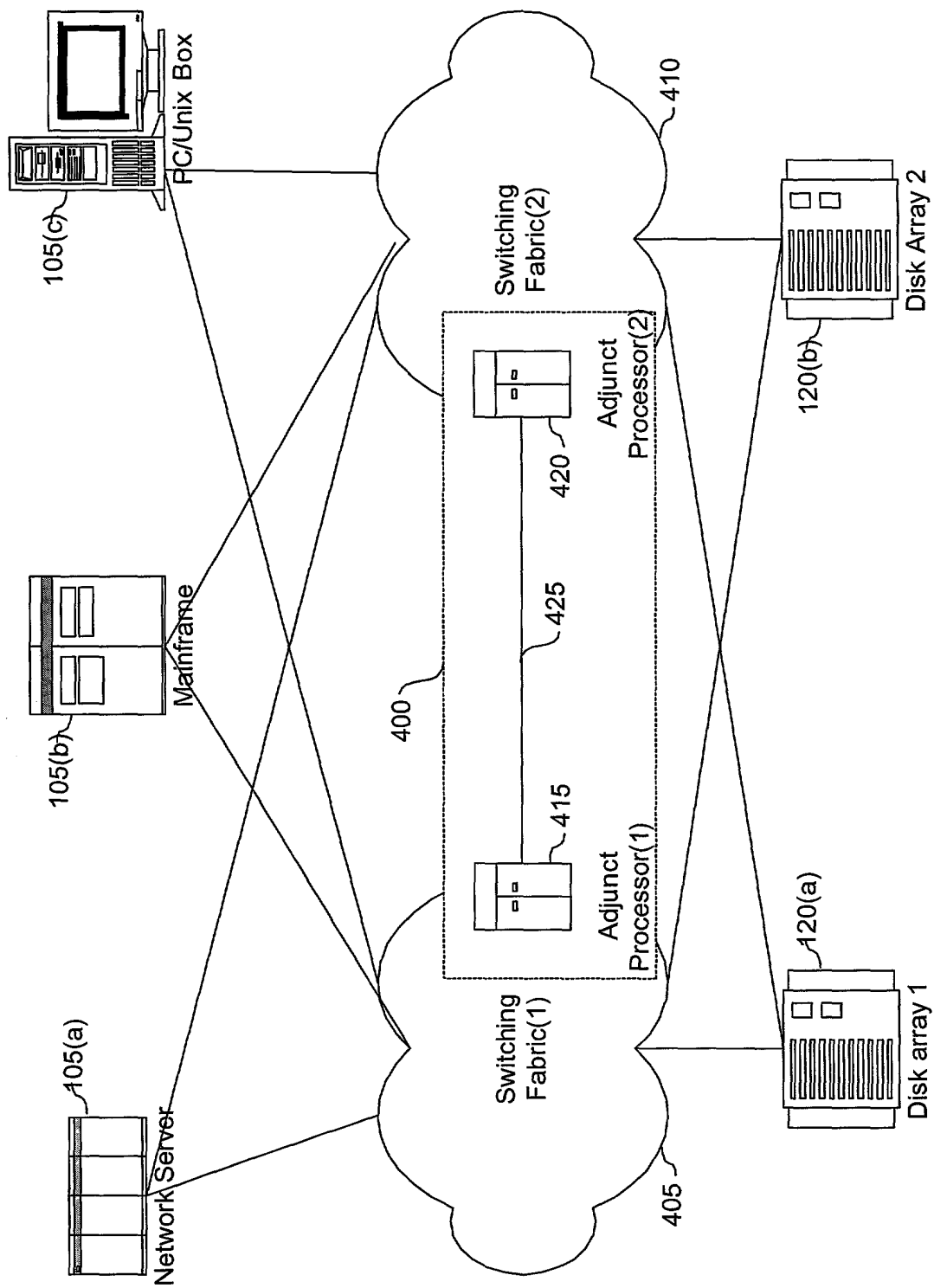


Figure 4

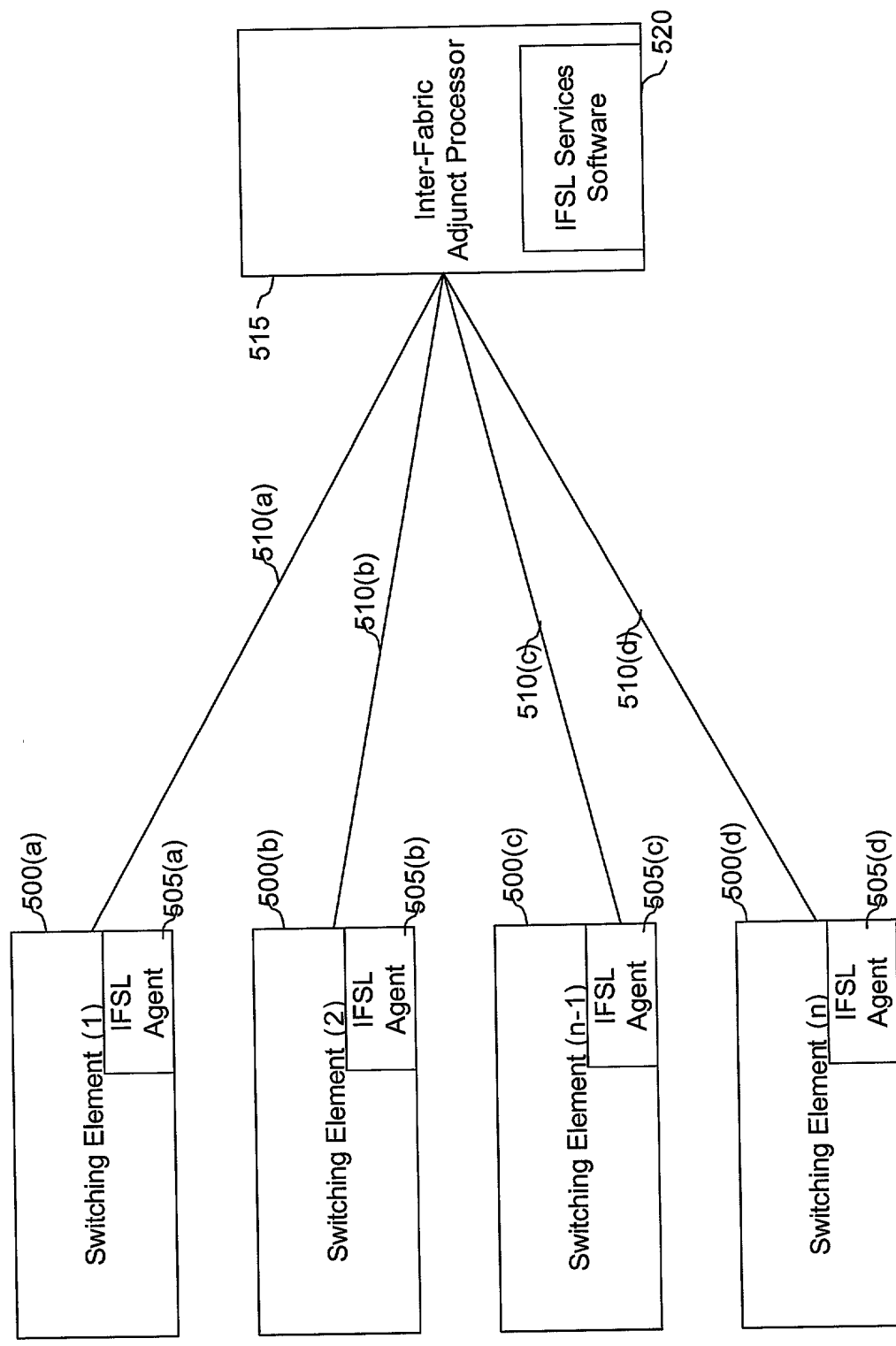


Figure 5

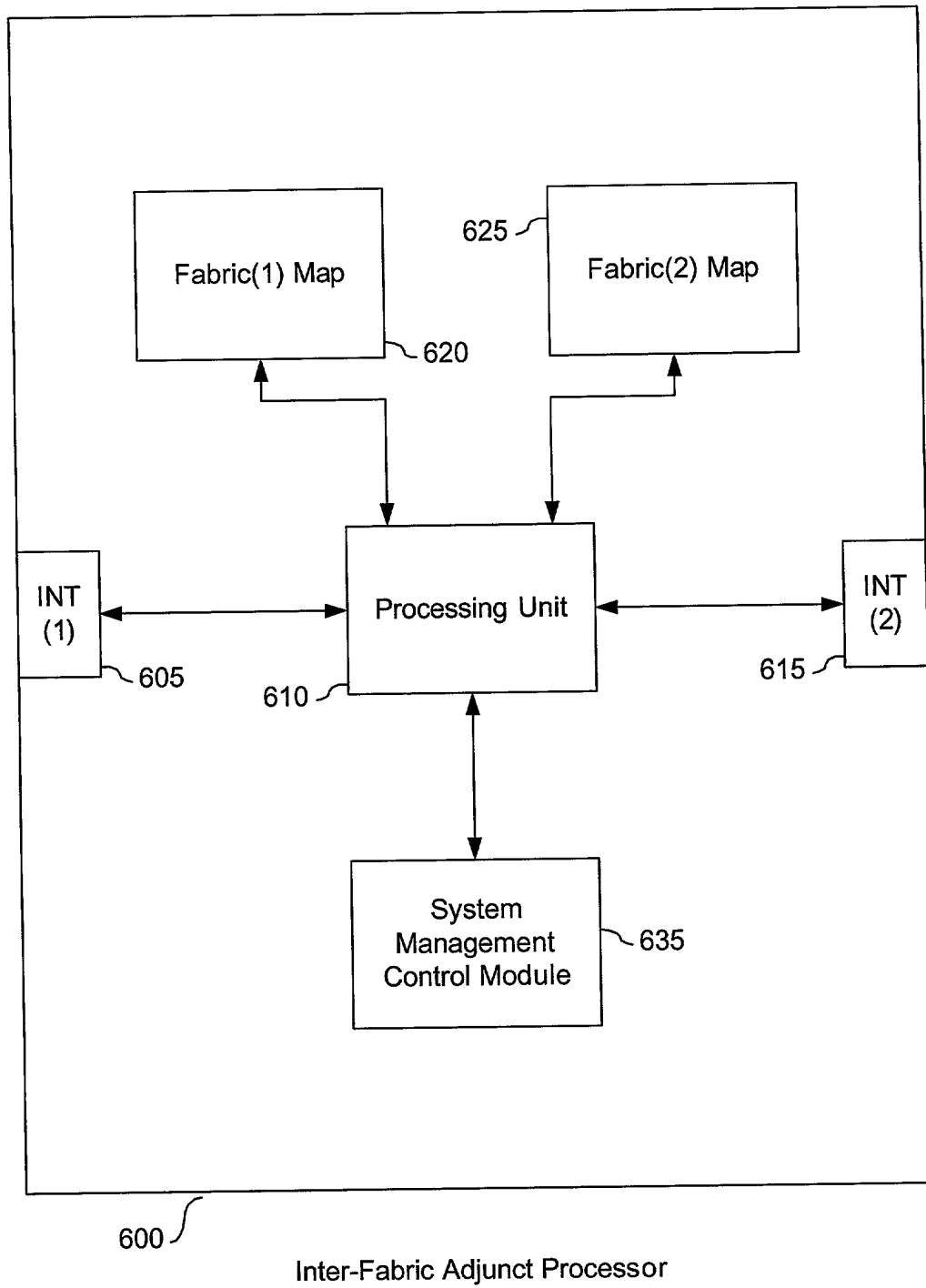


Figure 6

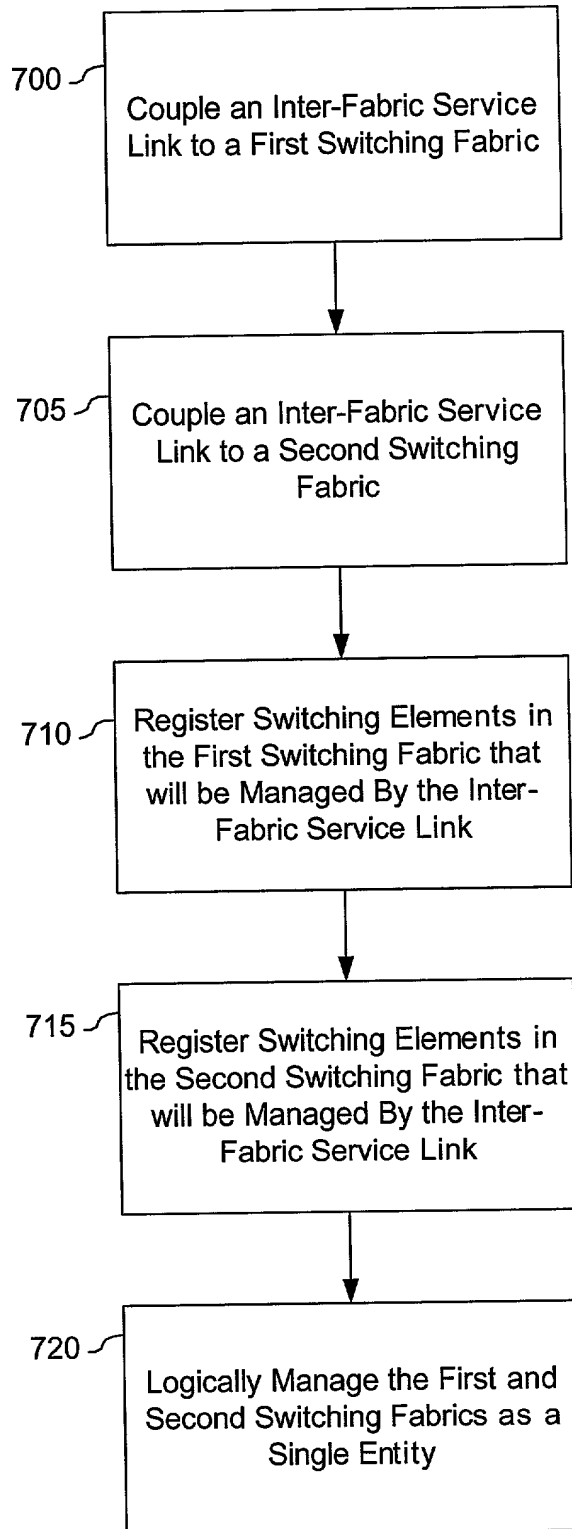


Figure 7

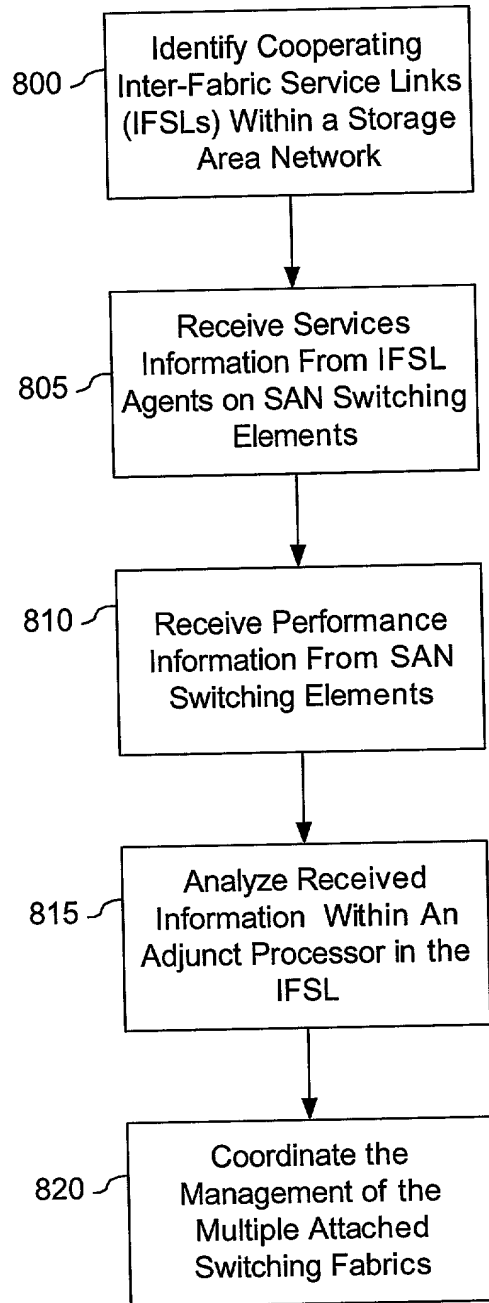


Figure 8



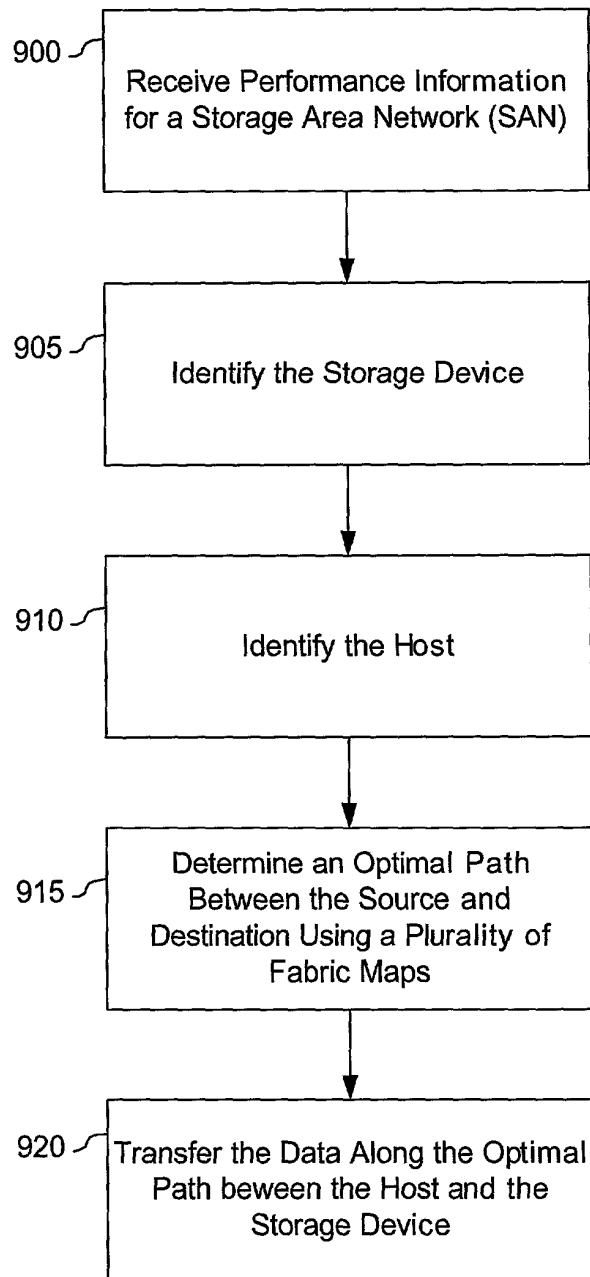


Figure 9